

# Surface Mount Fast Recovery Rectifiers

## **FEATURES:**

- For surface mounted applications
- Low profile package
- · Glass Passivated Chip Junction
- · Easy to pick and place
- Lead free in comply with EU RoHS 2011/65/EU directives

## **Circuit Diagram & Pin Configuration:**





#### **SMBF**

#### **Absolute Maximum Ratings and Characteristics**

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Parameter	Symbols	RS2ABF	RS2BBF	RS2DBF	RS2GBF	RS2JBF	RS2KBF	RS2MBF	Units
Maximum Repetitive Peak Reverse Voltage	$V_{RRM}$	50	100	200	400	600	800	1000	V
Maximum RMS voltage	$V_{RMS}$	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V <sub>DC</sub>	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current at $T_c$ = 125 °C	I <sub>F(AV)</sub>	2					А		
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load	I <sub>FSM</sub>	50					А		
Maximum Forward Voltage at 2 A	V <sub>F</sub>	1.3					V		
Maximum DC Reverse Current $T_a = 25  ^{\circ}\text{C}$ at Rated DC Blocking Voltage $T_a = 125  ^{\circ}\text{C}$	I <sub>R</sub>	5 100					μA		
Typical Junction Capacitance at V <sub>R</sub> =4V, f=1MHz	C <sub>j</sub>	28					pF		
Maximum Reverse Recovery Time (1)	t <sub>rr</sub>	150 250 500			00	ns			
Typical Thermal Resistance (2)	$R_{ heta_{JA}}$ $R_{ heta_{JC}}$	60 18					°C/W		
Operating and Storage Temperature Range	$T_{j}, T_{stg}$	-55 ~ +150					°C		

<sup>(1)</sup> Measured with  $I_F = 0.5 A$ ,  $I_R = 1 A$ ,  $I_{rr} = 0.25 A$ .

#### Marking

Type number	Marking code				
RS2ABF	R2AB				
RS2BBF	R2BB				
RS2DBF	R2DB				
RS2GBF	R2GB				
RS2JBF	R2JB				
RS2KBF	R2KB				
RS2MBF	R2MB				

<sup>( 2 )</sup> P.C.B. mounted with 2.0" X 2.0" (5 X 5 cm) copper pad areas.



Fig.1 Forward Current Derating Curve

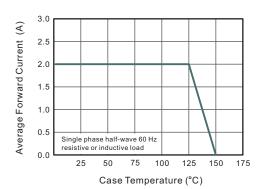


Fig.2 Typical Reverse Characteristics

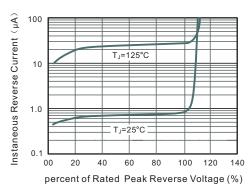


Fig.3 Typical Instaneous Forward Characteristics

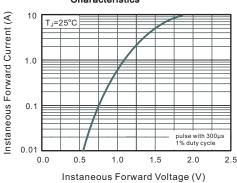


Fig.4 Typical Junction Capacitance

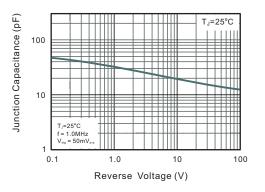
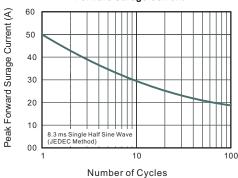


Fig.5 Maximum Non-Repetitive Peak Forward Surage Current

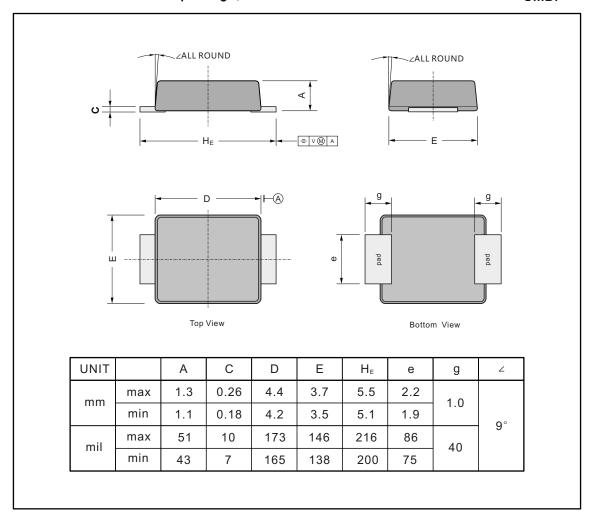




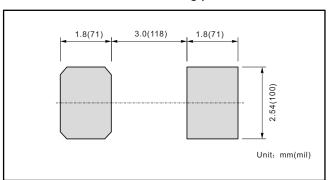
### PACKAGE OUTLINE

### Plastic surface mounted package; 2 leads

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### The recommended mounting pad size





## **NOTICE**

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