

Surface Mount Superfast Recovery Rectifier

FEATURES:

- For surface mounted applications
- Low profile package
- Glass Passivated Chip Junction
- Superfast reverse recovery time
- Lead free in comply with EU RoHS 2011/65/EU directives

Circuit Diagram & Pin Configuration:





Marking

| Type number | Marking code |
|-------------|--------------|
| ES1A | ES1A |
| ES1B | ES1B |
| ES1C | ES1C |
| ES1D | ES1D |
| ES1E | ES1E |
| ES1G | ES1G |
| ES1J | ES1J |

SMA/DO-214AC

Absolute Maximum Ratings and Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%.

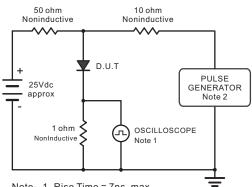
| Parameter | Symbols | ES1A | ES1B | ES1C | ES1D | ES1E | ES1G | ES1J | Units |
|---|--------------------|-------------|------|------|------|------|------|------|-------|
| Maximum Repetitive Peak Reverse Voltage | V_{RRM} | 50 | 100 | 150 | 200 | 300 | 400 | 600 | V |
| Maximum RMS voltage | V _{RMS} | 35 | 70 | 105 | 140 | 210 | 280 | 420 | V |
| Maximum DC Blocking Voltage | V _{DC} | 50 | 100 | 150 | 200 | 300 | 400 | 600 | V |
| Maximum Average Forward Rectified Current | I _{F(AV)} | 1 | | | | | | | |
| Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load (JEDEC Method) | I _{FSM} | 30 | | | | | | | А |
| Maximum Forward Voltage at 1 A | V _F | 1 1.25 1.70 | | | | | | 1.70 | V |
| Maximum DC Reverse Current Ta = 25 °C at Rated DC Blocking Voltage Ta =125 °C | I _R | 5 100 | | | | | | | μА |
| Typical Junction Capacitance at V _R =4V, f=1MHz | C _j | 15 | | | | | | | pF |
| Maximum Reverse Recovery Time (1) | t _{rr} | 35 | | | | | | | ns |
| Typical Thermal Resistance (2) | R _{0JA} | 75 | | | | | | | °C/W |
| Operating and Storage Temperature Range | T_{j},T_{stg} | -55 ~ +150 | | | | | | | |

⁽¹⁾ Measured with $I_F = 0.5 A$, $I_R = 1 A$, $I_{rr} = 0.25 A$.

⁽²⁾ P.C.B. mounted with 1.0 X 1.0" (2.54 X 2.54 cm) copper pad areas.



Fig.1 Reverse Recovery Time Characteristic And Test Circuit Diagram



- Note: 1. Rise Time = 7ns, max.

 Input Impedance = 1megohm,22pF.
 - 2. Ries Time =10ns, max. Source Impedance = 50 ohms.

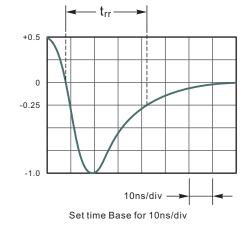


Fig.2 Maximum Average Forward Current Rating

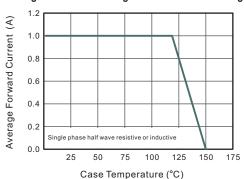


Fig.3 Typical Reverse Characteristics

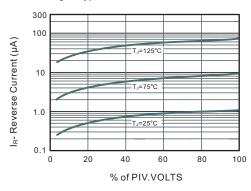


Fig.4 Typical Forward Characteristics

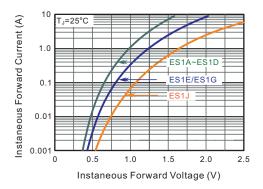


Fig.5 Typical Junction Capacitance

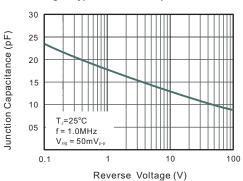
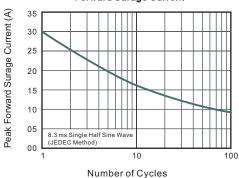


Fig.6 Maximum Non-Repetitive Peak Forward Surage Current

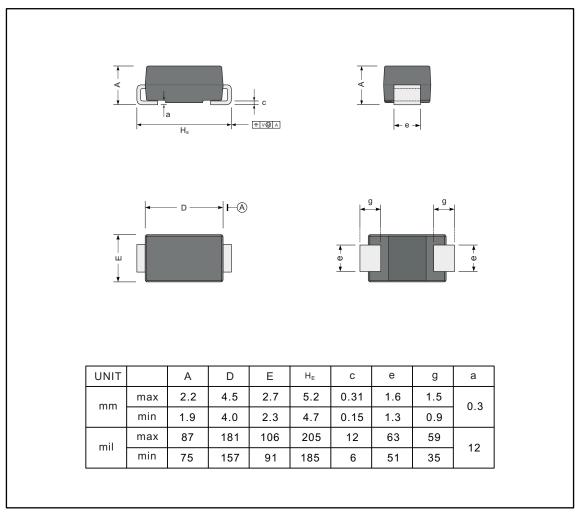




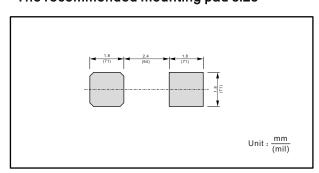
PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

SMA/DO-214AC



The recommended mounting pad size





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